TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSV)

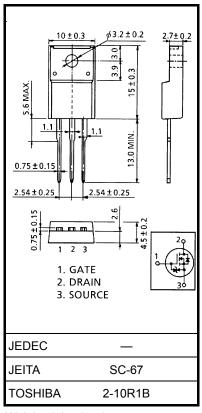
# 2SK3313

Chopper Regulatorand DC–DC Converter Applications Motor Drive Applications

- Fast reverse recovery time  $: t_{rr} = 90 \text{ ns (typ.)}$
- Built-in high-speed free-wheeling diode
- Low drain-source ON resistance  $: R_{DS} (ON) = 0.5 \Omega (typ.)$
- High forward transfer admittance  $|Y_{fs}| = 8.5 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 500 \ V)$
- Enhancement mode  $: V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ Ip} = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	500	V	
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		V <sub>DGR</sub>	500	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	12	А	
	Pulse (Note 1)	I <sub>DP</sub>	48	А	
Drain power dissipation	n (Tc = 25°C)	PD	40	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	324	mJ	
Avalanche current		I <sub>AR</sub>	12	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	4.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	62.5	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 3.83 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

Unit: mm

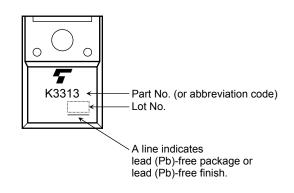
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	$V_{GS}$ = ±25 V, $V_{DS}$ = 0 V	_		±10	μA
Gate-source br	eakdown voltage	V <sub>(BR)</sub> GSS	I <sub>G</sub> = ±100 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V <sub>(BR)</sub> DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	500	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A		0.5	0.62	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A	3.0	8.5		S
Input capacitance	ce	C <sub>iss</sub>			2040		
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		210		pF
Output capacitance		C <sub>oss</sub>			630	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \qquad I_{D} = 6A \\ \downarrow \qquad \downarrow$		22	_	
	Turn-on time	t <sub>on</sub>		_	58	_	
	Fall time	t <sub>f</sub>			36	_	ns
	Turn-off time	t <sub>off</sub>	$V_{DD}$ ⇒200V Duty ≤1%, t <sub>w</sub> =10µs	_	180	_	
Total gate charge (Gate-source plus gate-drain)			_	45	_	nC	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		25		
Gate-drain ("miller") charge		Q <sub>gd</sub>			20		—

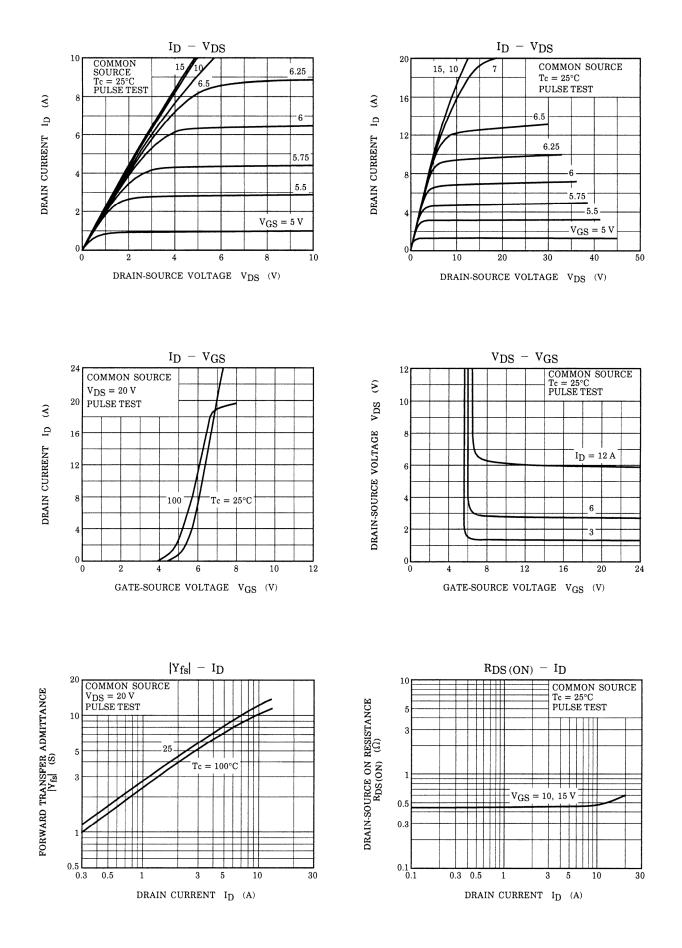
## Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	12	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	48	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V dI <sub>DR</sub> / dt = 100 A / μs		90	160	ns
Reverse recovery charge	Qrr			0.25		μC

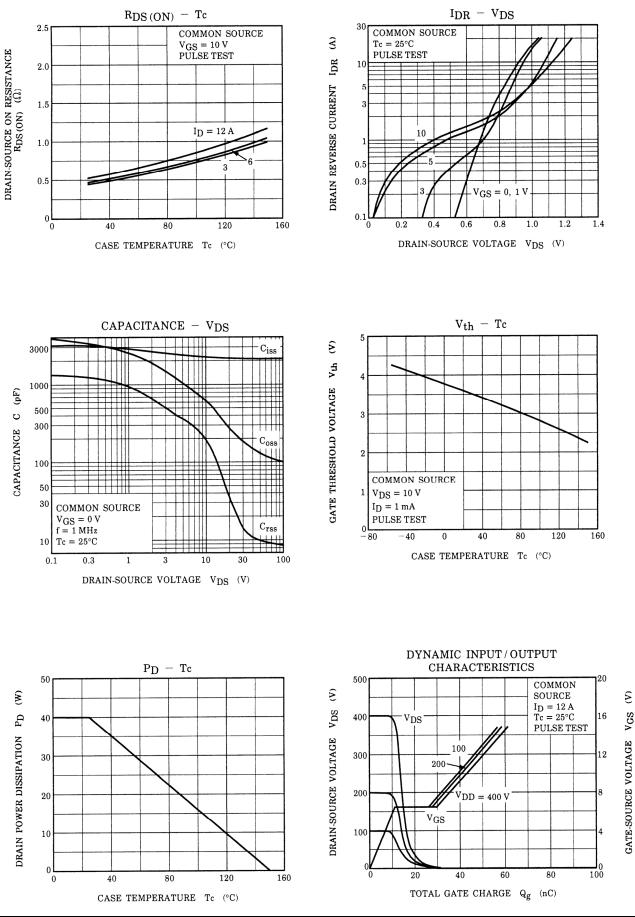
## Marking

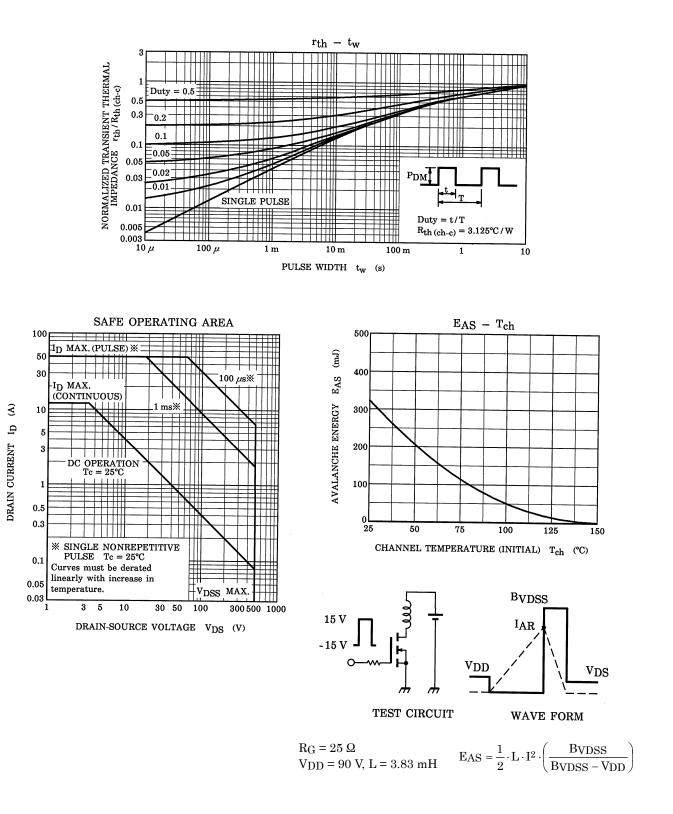


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